

REMARKS

Claims 1-9 and 15-20 remain pending for the present application.

Claim 21 has been added. Applicants respectively request reconsideration of the above referenced application in view of the above claim amendments and the remarks presented below.

35 U.S.C. Section 112 rejections:

The above referenced Office Action reject Claim 17 as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. More specifically, the present Office Action states the presence of the word "of" connecting "branch" and "target instructions" is the cause of the problem. Applicants respectfully traverse.

Applicants point out that one skilled in the art would know that generally instructions may cause a branch of flow control. The present application states: "For example, if a target instruction includes a branch, the address of the instruction to which it branches may be recorded along with the branch which has been executed." (Page 13, lines 19-22). Applicants respectfully point out that as target instructions execute, a target instruction may cause a branch of flow of control or sequence of instructions to occur which causes a branch to be executed. Therefore, the number of times a

branch executes as a result of a change in flow control from target instructions can be counted. Thus, Applicants respectfully assert that one skilled in the art would understand Claim 17 to particularly point out and distinctly claim the subject matter which Applicants regard as the invention and therefore Claim 17 is definite within the meaning of 35 U.S.C. Section 112.

35 U.S.C Section 102 Rejections:

Claims 1, 3, 8-9, and 15 are rejected under 35 U.S.C. Section 102(e) as being anticipated by Babaian et al. Applicants respectfully traverses.

Applicants have amended independent Claim 1 to include the feature of using additional registers during execution for holding an official state of a target processor. Applicants respectfully assert that independent Claims 3 and 8 recite similar limitations and have been amended in a substantially similar manner.

Applicants point out that this is completely different from the cited references, which discloses a register file used for storing execution data. There appears to be no recitation regarding additional registers. Accordingly, Applicants respectively assert that the claimed invention is not anticipated by cited references within the meaning of 35 U.S.C. Section 102(e).

With respect to Claims 9 and 15, Applicants respectfully state that Claims 9 and 15 depend from allowable independent Claims 8 and 1 respectively and recite further features of the present claimed invention. Therefore, Applicants respectfully assert that Claims 9 and 15 are allowable as depending from allowable base Claims.

35 U.S.C Section 103 Rejections

For the same rationale as described above, regarding additional registers, Applicants submit that these limitations are not shown or suggested by the cited references. Accordingly, the claimed invention is not rendered obvious by the cited references within the meaning of 35 U.S.C Section 103.

New Claims

Applicants respectfully points out that Claim 21 includes the feature of using additional registers for being used for holding the official state of a target processor which is included in Claim 1. Based on the rationale above with respect to Claim 1, Applicants respectfully assert that Claim 21 is in condition for allowance. Furthermore, Claim 21 includes the feature of buffer that stores working memory state changes and official memory state changes which are not taught, shown, or suggested by the above cited references.

CONCLUSION

The Examiner is urged to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Please charge any additional fees or apply any credits to our PTO deposit account number: 50-4160.

Respectfully submitted,

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